

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Inventor(s): ELLIS, Frampton
 Atty.Dkt: 081498/0279404
 Appln. No.: Unknown/
 Filed: Herewith
 FOR: GLOBAL NETWORK COMPUTERS

Group Art Unit: Unknown
 Examiner: Unknown



Date: June 20, 2001

IDS LETTER CITING APPLICATION(S) AND ATTACHING PTO-1449/DOCUMENTS

The Examiner's attention is directed to the following application(s) of:

Examiner's Initials*	FIRST INVENTOR (Last Name Only)	APPLICATION NO. & Filing Date	ENCLOSED
	ELLIS	09/669,730, filed September 26, 2000	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/571,558, filed May 16, 2000	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/320,660, filed May 27, 1999	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/315,026, filed May 20, 1999	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/085,755, filed May 27, 1998	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	08/980,058, filed November 26, 1997	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other
	ELLIS	09/213,875, filed December 17, 1998	<input type="checkbox"/> Spec <input type="checkbox"/> Drawings <input type="checkbox"/> Current Claims <input type="checkbox"/> Other

PLEASE DO NOT PRINT the above information on the patent which results from the subject application.

Consideration of each listed application is earnestly solicited since unpublished patent applications are contemplated as IDS material; see the exception in Rule 98(a)(2)(iii) and note the penultimate sentence of MPEP 609.

Further, in keeping with MPEP 609, Subsec. C(2), 2nd para., line 10 to end of the paragraph (especially note lines 18-25) **PLEASE RETURN A COPY OF THIS LETTER** with the Examiner's initials adjacent each above listing so that applicant will know that each listed application has been considered as required by PTO policy. Thanks.

Secondly, please consider each enclosed document which is listed on the attached Form

PTO-1449 and return a copy of that form with the Examiner's initials adjacent each citation.

Thanks again.

Respectfully submitted

A handwritten signature in black ink, appearing to read 'Dale S. Lazar', written in a cursive style.

Dale S. Lazar

*The Examiner's initials adjacent a citation indicates he/she has considered the cited application relative to the subject application.

FORM PTO-1449 (modified)
To: U.S. Department of Commerce
(P/W FORM-PAT-1449)
Patent and Trademark Office

Atty.
Dkt. No.

M#

Client Ref.

0279404

GNC16CON

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Applicant: ELLIS

Continuation of Appln. No.: 09/315,026

Filing Date: May 29, 1999

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Examiner: Unknown

Group Art Unit: Unknown

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	BR	4,918,596	04/1990	NAKANO			
	CR	4,969,092	11/1990	SHORTER			
	DR	5,025,369	06/1991	SCHWARTZ			
	ER	5,031,089	07/1991	LIU et al.			
	FR	5,103,393	04/1992	HARRIS et al.			
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Examiner

Date Considered:

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FORM PTO-1449 (modified) To: U.S. Department of Commerce (PW FORM PAT-1449) Patent and Trademark Office					Atty. Dkt. No.	M#	Client Ref.	
						0279404	GNC16CON1	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Filing Date: May 29, 1999			
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		Document Number	Date MM/YYYY	Country	Inventor Name			
	WR	WO 98/26366	06/1998	WIPO	Ellis			
	XR	0 853 279	07/1998	EUROPE	Nagaratina et al.			
	YR	WO 94/01964	01/1994	WIPO	Craig			
	ZR	WO 95/01060	01/1995	WIPO	Kramer et al.			
	AAR	EP 0 840216A2	05/1998	Europe	Chung et al.			
OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)								
BBR "Ein-Chip-Firewall," Elektroniknet Top News, XP-002164257								
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Date: June 20, 2001		Page	3	of	6	Examiner: Unknown	Group Art Unit: Unknown
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OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)								
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	LR	Taisuke Boku, Hiroshi Nakamura, Kisaburo Nakazawa, and Yoichi Iwasaki, The Architecture of Massively Parallel Processor CP-PACS, "IEEE 1997," pgs. 31-40.						
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